

FIG.1

(LOGICAL PARTITIONING BOUNDARY)

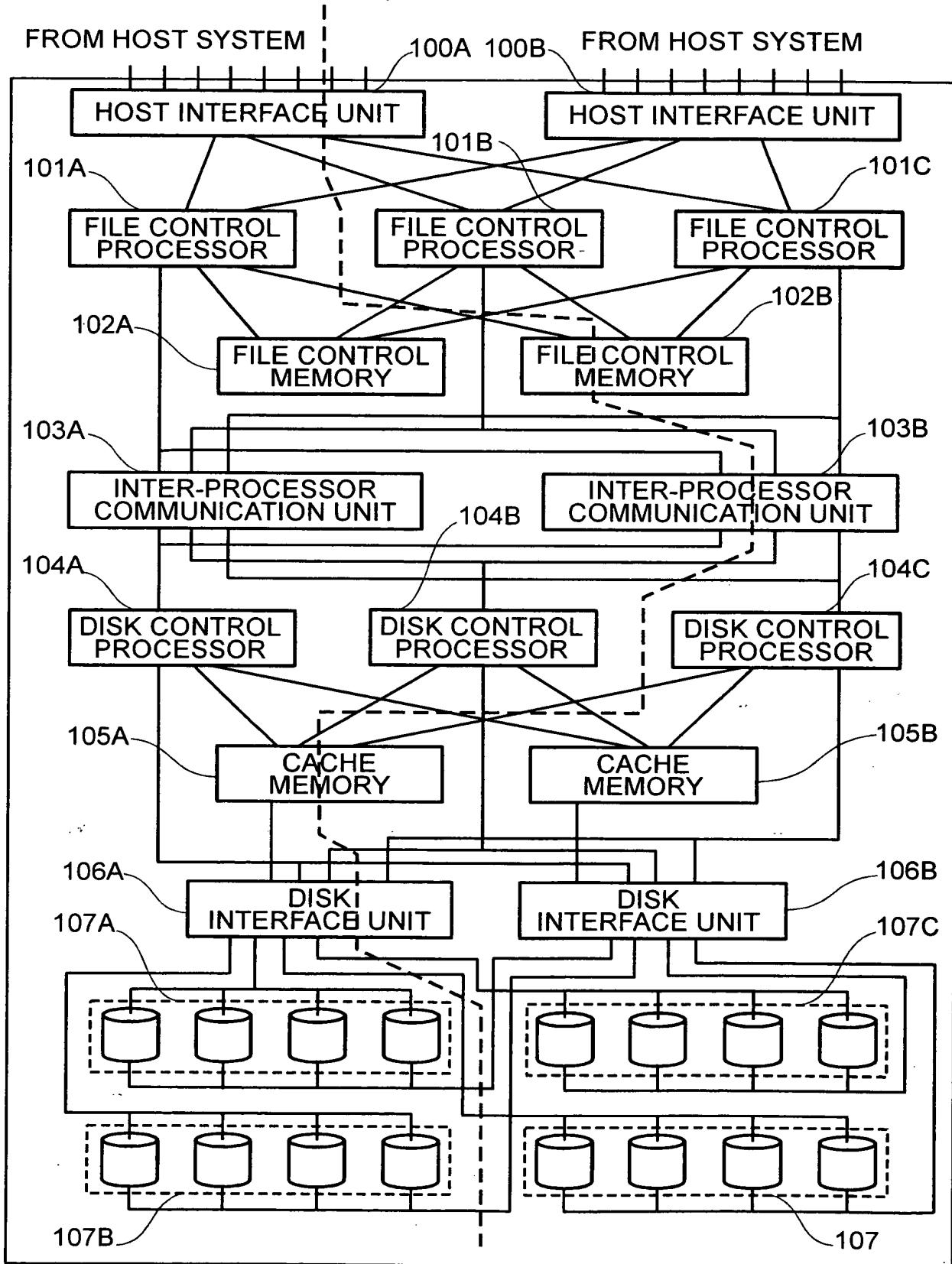
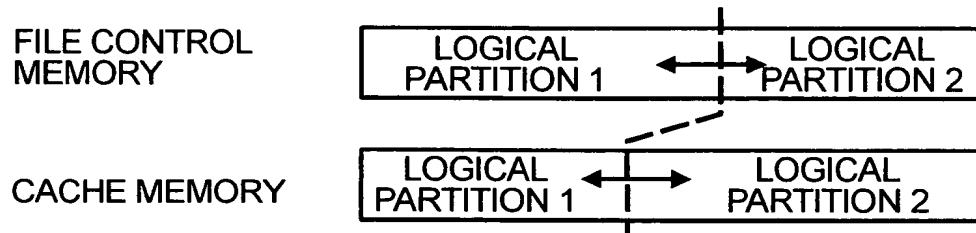
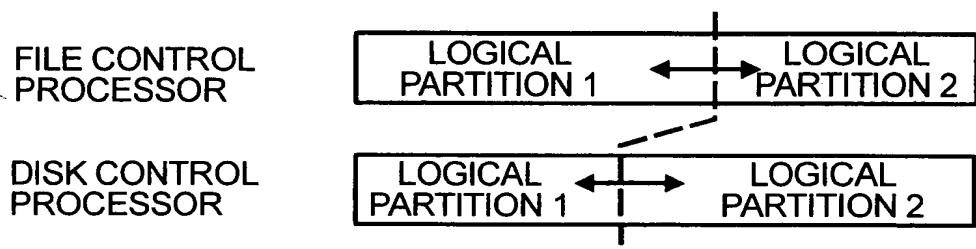


FIG.2



	IMPORTANCE IS ATTACHED TO READ DATA TRANSFER RATE	IMPORTANCE IS ATTACHED TO RESPONSE TIME	LARGE AREA RANDOM ACCESS	SEQUENTIAL ACCESS
FILE CONTROL MEMORY	ALLOCATION SMALL	ALLOCATION LARGE	ALLOCATION SMALL	ALLOCATION LARGE
CACHE MEMORY	ALLOCATION LARGE	ALLOCATION SMALL	ALLOCATION SMALL	ALLOCATION LARGE

FIG.3



	SMALL NUMBER OF LARGE FILE ACCESS	LARGE NUMBER OF SMALL FILE ACCESS	NOT REQUIRED HIGH PERFORMANCE	REQUIRED HIGH PERFORMANCE
FILE CONTROL PROCESSOR	ALLOCATION SMALL	ALLOCATION LARGE	ALLOCATION SMALL	ALLOCATION LARGE
DISK CONTROL PROCESSOR	ALLOCATION LARGE	ALLOCATION SMALL	ALLOCATION SMALL	ALLOCATION LARGE

FIG.4

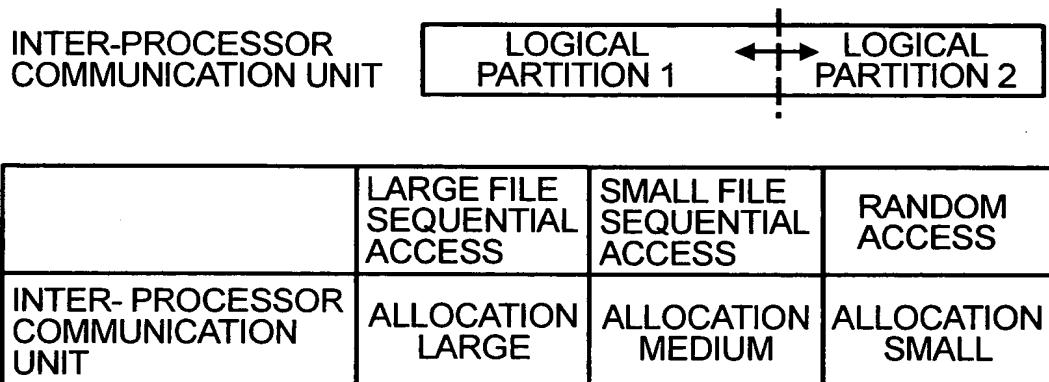
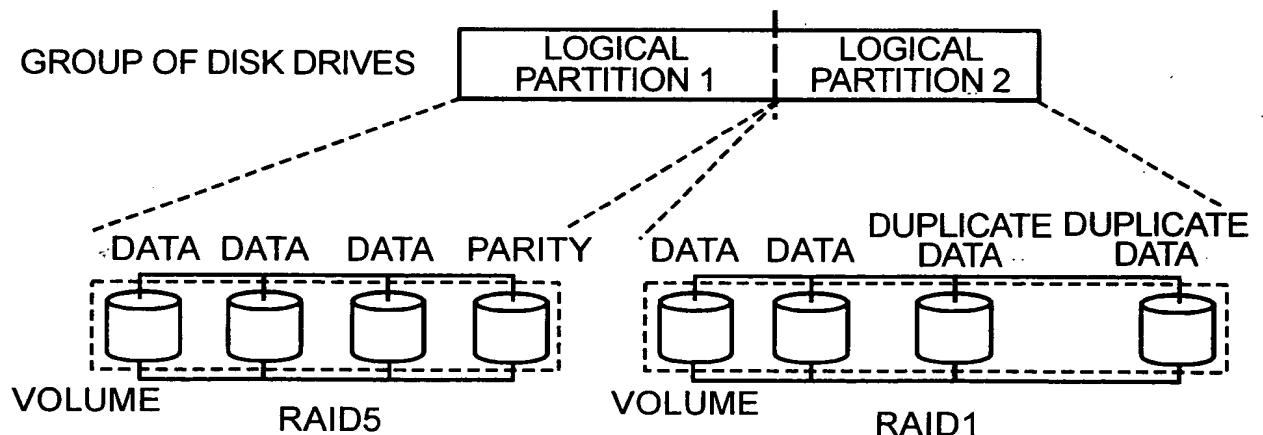


FIG.5



	CAPACITY PRIORITIZED	ACCESS PERFORMANCE PRIORITIZED
GROUP OF DISK DRIVES	RAID 5 VOLUME ALLOCATION	RAID 1 VOLUME ALLOCATION

FIG.6

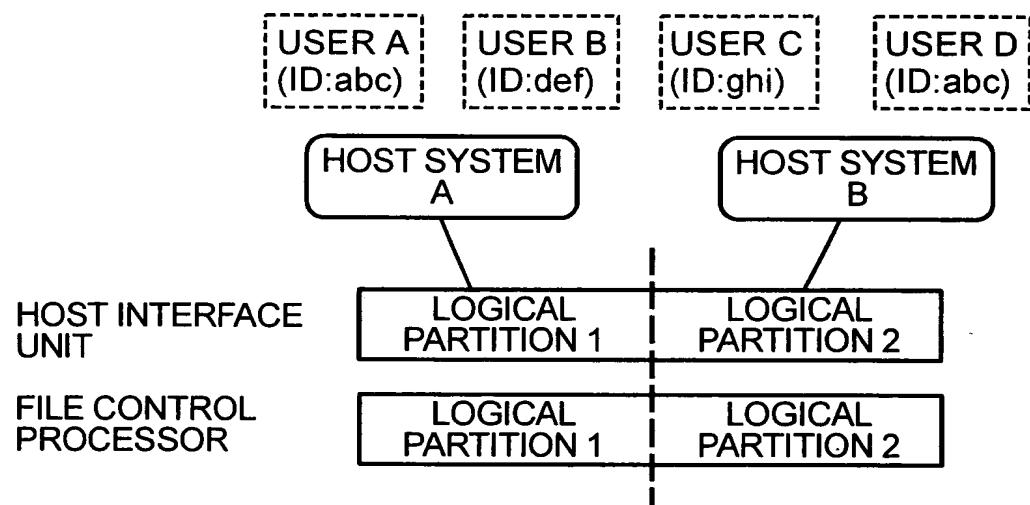


FIG.7

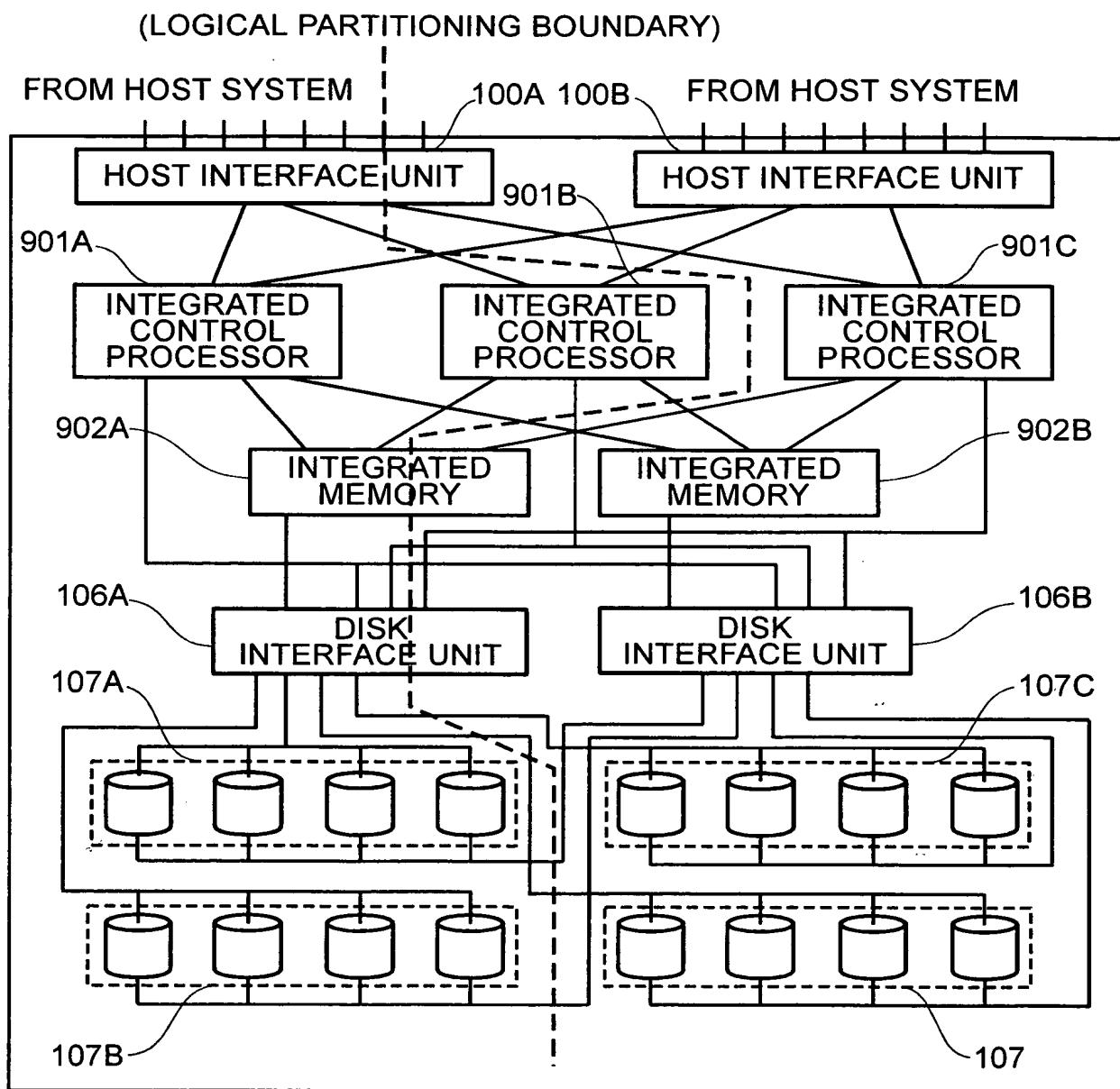


FIG.8

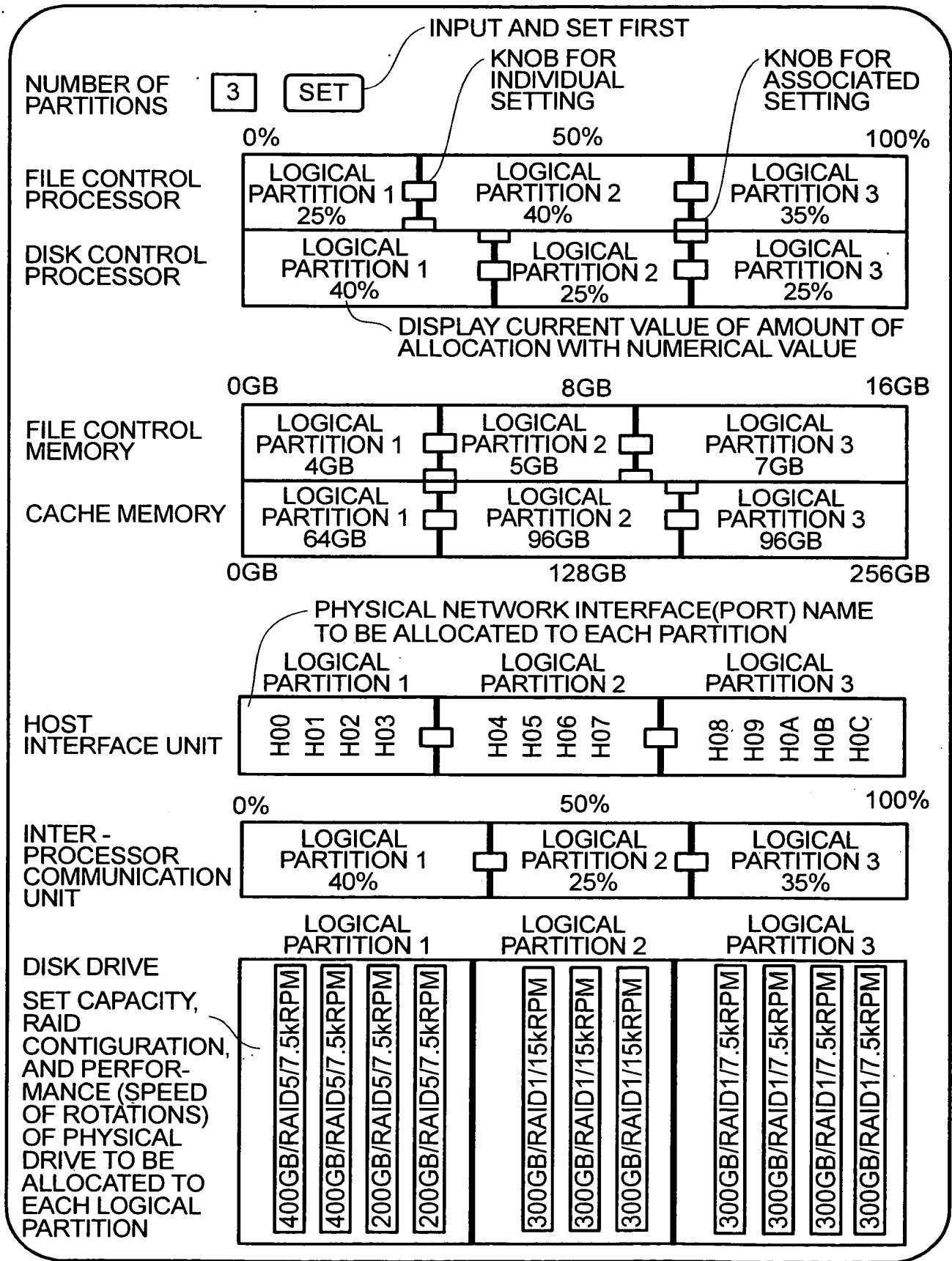


FIG.9

	LOGICAL PARTITION 1	LOGICAL PARTITION 2	LOGICAL PARTITION 3	NOT ALLOCATED YET
FILE CONTROL PROCESSOR 1	100%	0%	0%	0%
FILE CONTROL PROCESSOR 2	0%	50%	50%	0%
FILE CONTROL PROCESSOR 3	0%	0%	0%	100%
DISK CONTROL PROCESSOR 1	100%	0%	0%	0%
DISK CONTROL PROCESSOR 2	0%	100%	0%	0%
DISK CONTROL PROCESSOR 3	0%	0%	100%	0%
FILE CONTROL MEMORY	4GB	5GB	7GB	2GB
CACHE MEMORY	64GB	96GB	96GB	0GB
HOST INTERFACE UNIT	H00 H01 H02 H03	H04 H05 H06 H07	H08 H09 H0A H0B	H0C
INTER-PROCESSOR COMMUNICATION	40%	25%	35%	0%
DISK DRVE	400GB/RAID5(DR#1.2.3.4)/7.5kRPM 400GB/RAID5(DR#5.6.7.8)/7.5kRPM 200GB/RAID5(DR#9.10.11.12)/7.5kRPM 200GB/RAID5(DR#13.14.15.16)/7.5kRPM	300GB/RAID1(DR#21.22)/15kRPM 300GB/RAID1(DR#23.24)/15kRPM 300GB/RAID1(DR#25.26)/15kRPM	300GB/RAID1(DR#31.32)/7.5kRPM 300GB/RAID1(DR#33.34)/7.5kRPM 300GB/RAID1(DR#35.36)/7.5kRPM 300GB/RAID1(DR#37.38)/7.5kRPM	

FIG.10

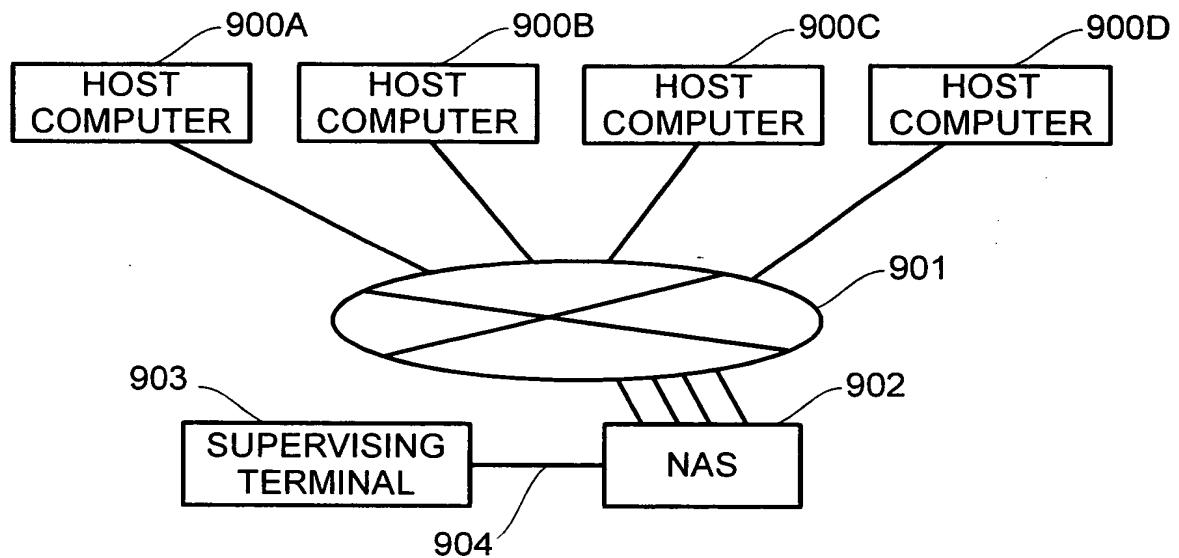


FIG.11

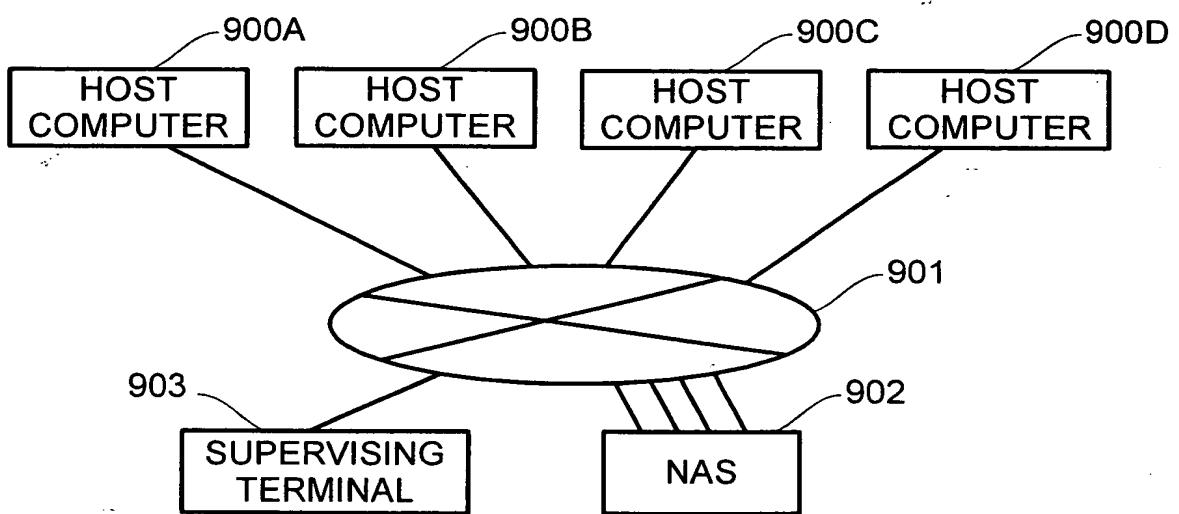


FIG.12

